Application Serial No. 10/625,287   PATENT
Reply to office action of May 25, 2006   Docket: CU-3301

Amendments To The Specification

Please replace paragraph [0034] in the specification page 10, line 17 to page 11, line 11 with the following amended paragraph:

[0034] The second shift register section 240 includes a predetermined number of second switches ~~SW30~SW58~~ **SW31~SW60** and a predetermined number of second shift registers SR31~SR60 ~~where SR31 shifts the signal outputted from the SR30~~. The second switches ~~SW30~SW58~~ **SW31~SW60** are switched by the output enable signal OES and then selects either the second vertical starting signal STV2 or an internally shifted signal. Further, when the internally shifted signal is selected by the switching operation of the predetermined number of second switches ~~SW30~SW58~~ **SW31~SW60**, the second shift registers ~~SR32~SR58~~ **SR31~SR60** receive the second vertical starting signal STV2 and then output it after sequentially shifting it. Also, when the second vertical starting signal STV2 is selected by the switching operation of the predetermined number of second switches ~~SW30~SW58~~ **SW31~SW60**, the second shift registers ~~SR32~SR58~~ **SR31~SR60** receive the second vertical starting signal STV2 and then output a predetermined number of second output signals at the same time without shifting.

Please replace paragraph [0035] in the specification page 11, lines 12-23 with the following amended paragraph:

[0035] For instance, the switch ~~SW30~~ **SW31** of the second switches ~~SW30~SW58~~ **SW31~SW60** switches to an output terminal of the shift register SR31 of the second shift registers SR31~SR60 during the active address interval and switches to an input terminal of the shift register SR30 of the first shift register section 220 during the vertical blanking interval. Also, the switch ~~SW30~~ **SW32** of the second switches ~~SW30~SW58~~ **SW31~SW60** switches to an output terminal of the shift register SR32 of the second shift registers SR31~SR60 during the active address interval and switches to an input terminal of the shift

register ~~SR34~~ <u>SR30</u> of the first shift register section 220 during the vertical blanking interval.